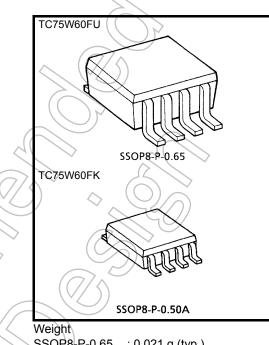
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC75W60FU, TC75W60FK

Dual Operational Amplifier

Features

- High slew rate : SR (V_{DD} = 3 V) = 5.1 V/ μ s (typ.)
- Single and dual power Supply operations are possible. : V_DD = ± 0.9 to 3.5 V or 1.8 to 7 V
- Lower supply current than general-purpose bipolar type op amps : I_{DD} (V_{DD} = 3 V) = 660 μ A (typ.)
- The internally phase compensated operational amplifier.
- Small package



SSOP8-P-0.65 : 0.021 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

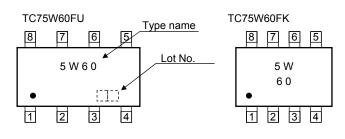
Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Supply voltage		V _{DD} , V _{SS}	(7/	V	
Differential input vo	Itage		±7	V	
Input voltage		VIN	V _{DD} to V _{SS}	V	
Power dissipation	TC75W60FU	D -	250	mW	
	TC75W60FK	PD	200	IIIVV	
Operating temperature		Topr	-40 to 85	°C	
Storage temperature		T _{stg}	–55 to 125	°C	

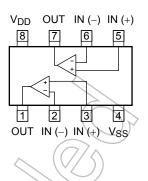
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Marking (top view)



Pin Connection (top view)



Electrical Characteristics

DC Characteristics ($V_{DD} = 3.0 V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V _{IO}	_	$R_{S} = 1 k\Omega$	- (2	7	mV
Input offset current	Ι _{ΙΟ}	_		-(~ _	pА
Input bias current	lj	_		K	-21/)) —	pА
Common mode input voltage	CMVIN		() - ()	0.0	50	2.1	V
Voltage gain (open loop)	GV		$\mathcal{A}(\mathcal{O}) = \mathcal{O}(\mathcal{O})$	60	70	_	dB
Maximum autaut valtaga	V _{OH}	70	RL = 100 kΩ	2.9		_	V
Maximum output voltage	V _{OL}	\mathcal{A}	RL = 100 kΩ) —	_	0.1	v
Common mode rejection ratio	CMRR <	(-)	$V_{IN} = 0.0 \text{ to } 2.1 \text{ V}$	54	70	_	dB
Suuply voltage rejection ratio	SVRR	$\langle \rightarrow \rangle$	V _{DD} = 1.8 to 7.0 V	60	70	_	dB
Supply current	IDD)+		_	660	1000	μA
Source current	Isource	2	~	330	700	_	μA
Sink current	Isink			600	1250	_	μA

DC Characteristics ($V_{DD} = 1.8 V$, $V_{SS} = GND$, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	VIO	\mathbb{N}	$R_S = 1 k\Omega$	_	2	7	mV
Input offset current	lio			_	1	_	pА
Input bias current	lı İ	Ý	_	_	1	_	pА
Common mode input voltage	CMVIN	_	_	0.3	_	0.9	V
Voltage gain (open loop)	Gv		—	_	70	_	dB
maximum output voltage	VOH		$R_L = 100 \text{ k}\Omega$	1.7		_	v
	VOL		$R_L = 100 \text{ k}\Omega$	_		0.1	
Common mode rejection ratio	CMRR		V _{IN} = 0.3 to 0.9 V	50	60	_	dB
Supply current	∕ I _{DD}		—	_	600	900	μA
Source current	I _{source}	_	—	300	700	—	μA
Sink current	I _{sink}		_	550	1150	_	μA

AC Characteristics (V_{DD} = 3.0 V, V_{SS} = GND, Ta = 25°C)

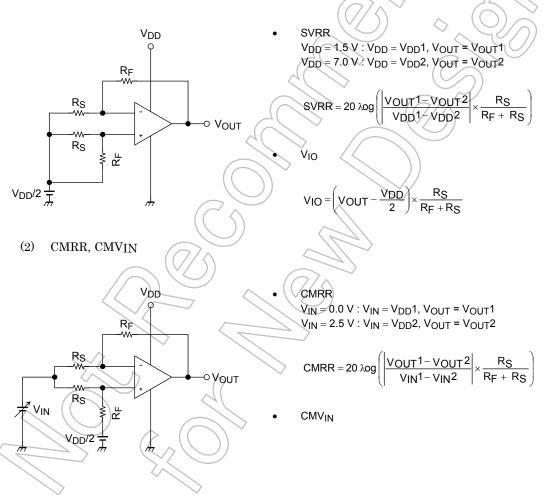
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Slew rate	SR	_	—	_	5.1	_	V/μs
Unity gain cross frequency	f _T	_			3.7		MHz

AC Characteristics ($V_{DD} = 1.8 V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

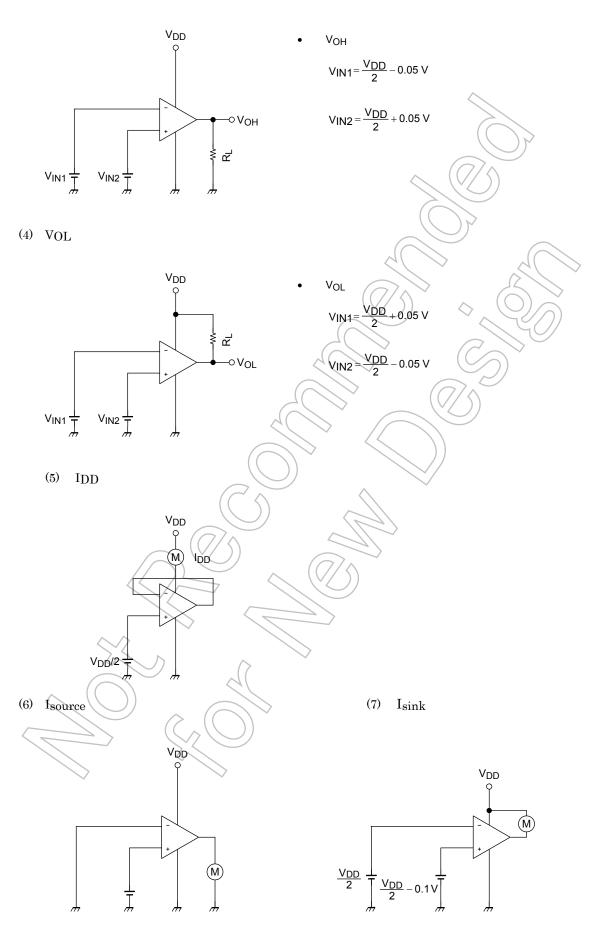
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Slew rate	SR	_	- ~ (0	'/A	4.0	_	V/µs
Unity gain cross frequency	f _T	_	- //*	Ą	3.0		MHz

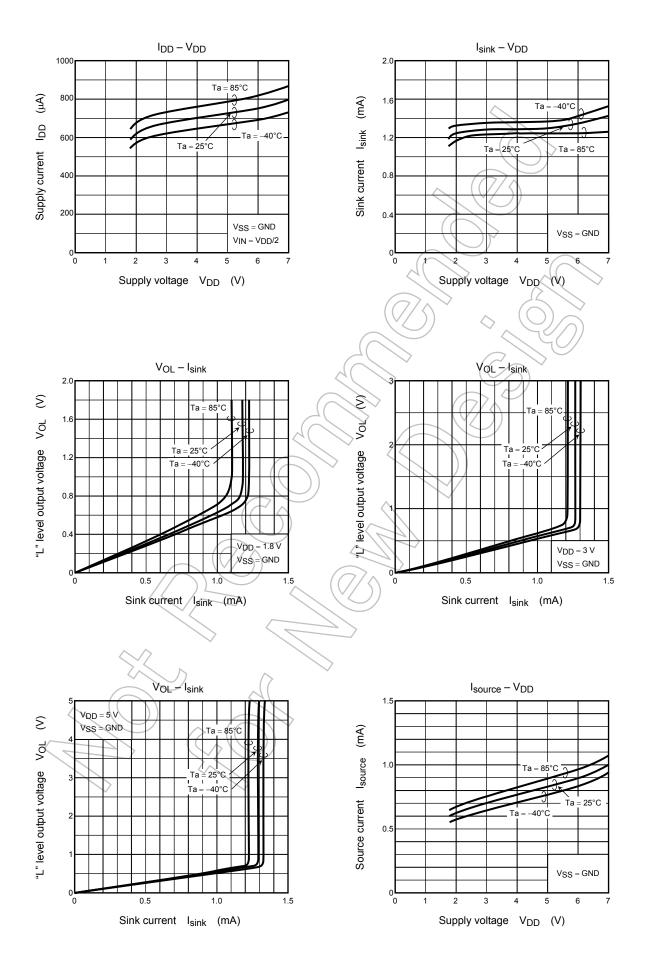
TEST CIRCUIT

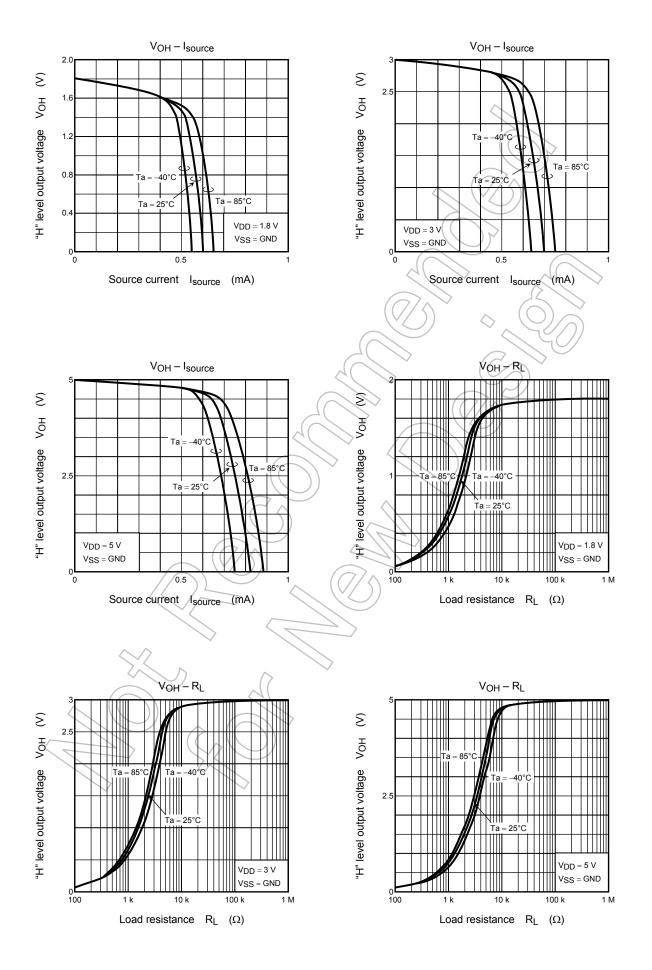


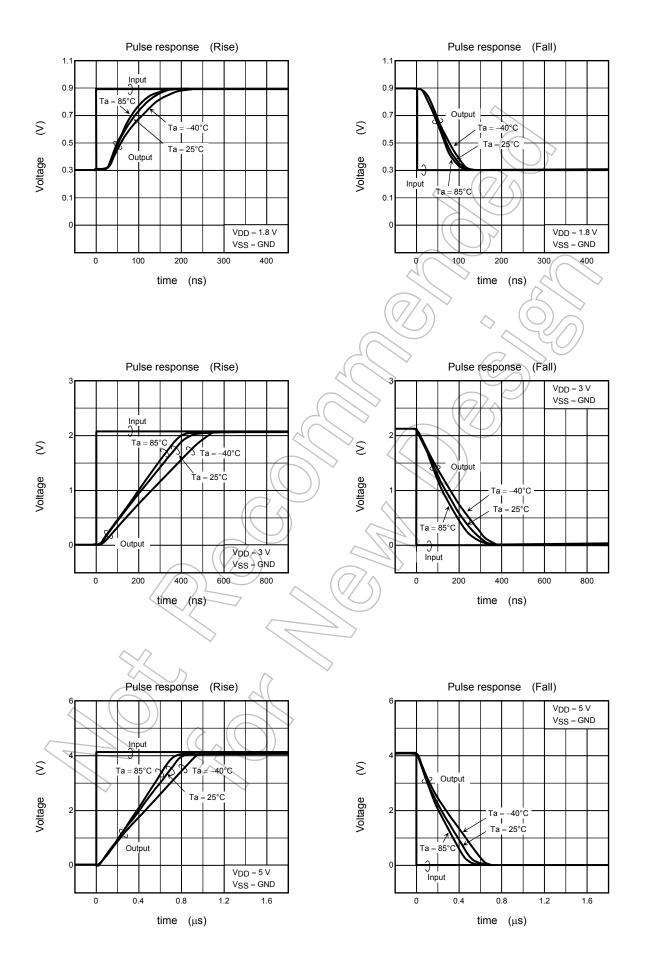


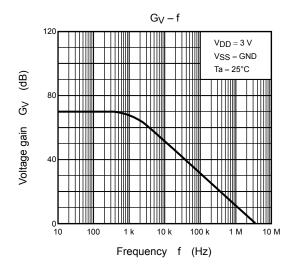
(3) VOH

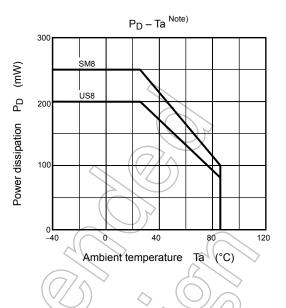












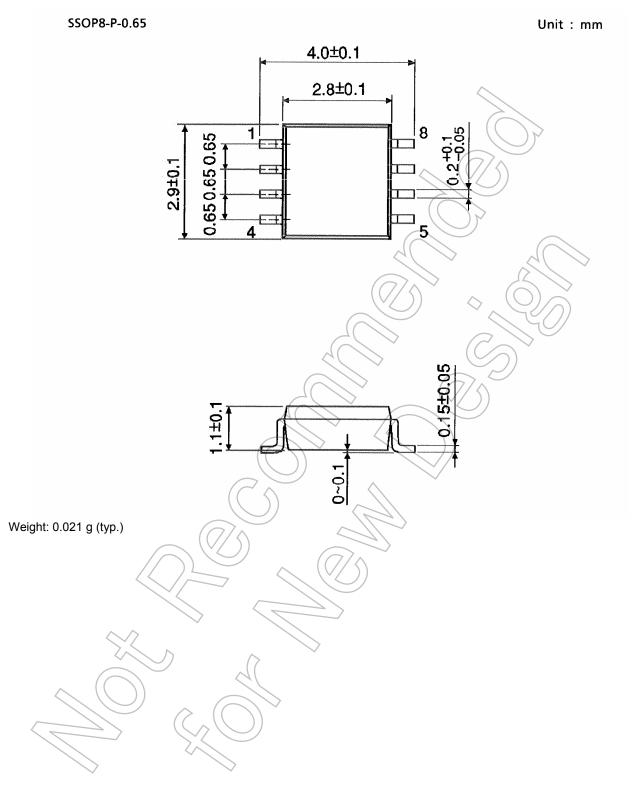
Note):

These power dissipation curves are given by measurement of only IC on the air and, in general, it become higher when mounted on

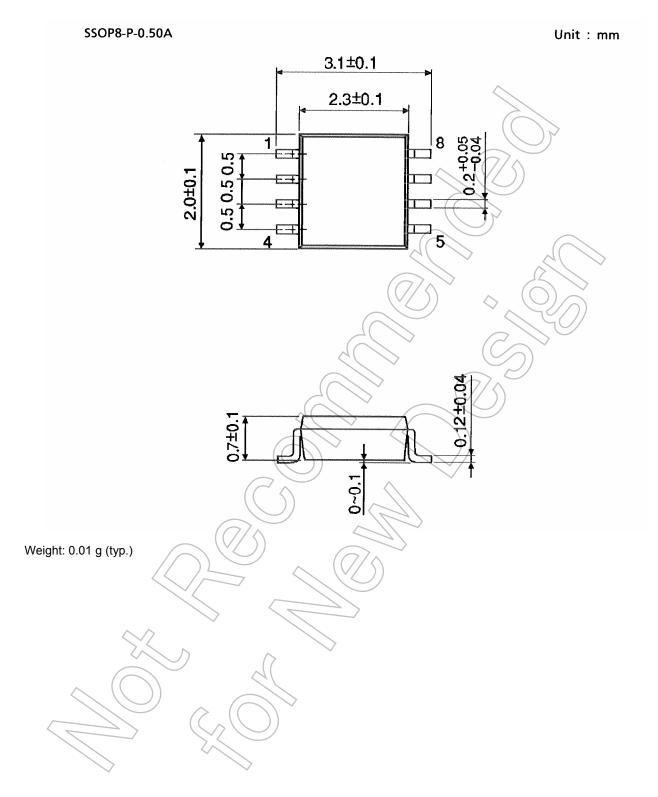
PCB.

Since the power dissipation depends on mounted condition, please be sure to design.

Package Dimensions



Package Dimensions



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